

FIG. I

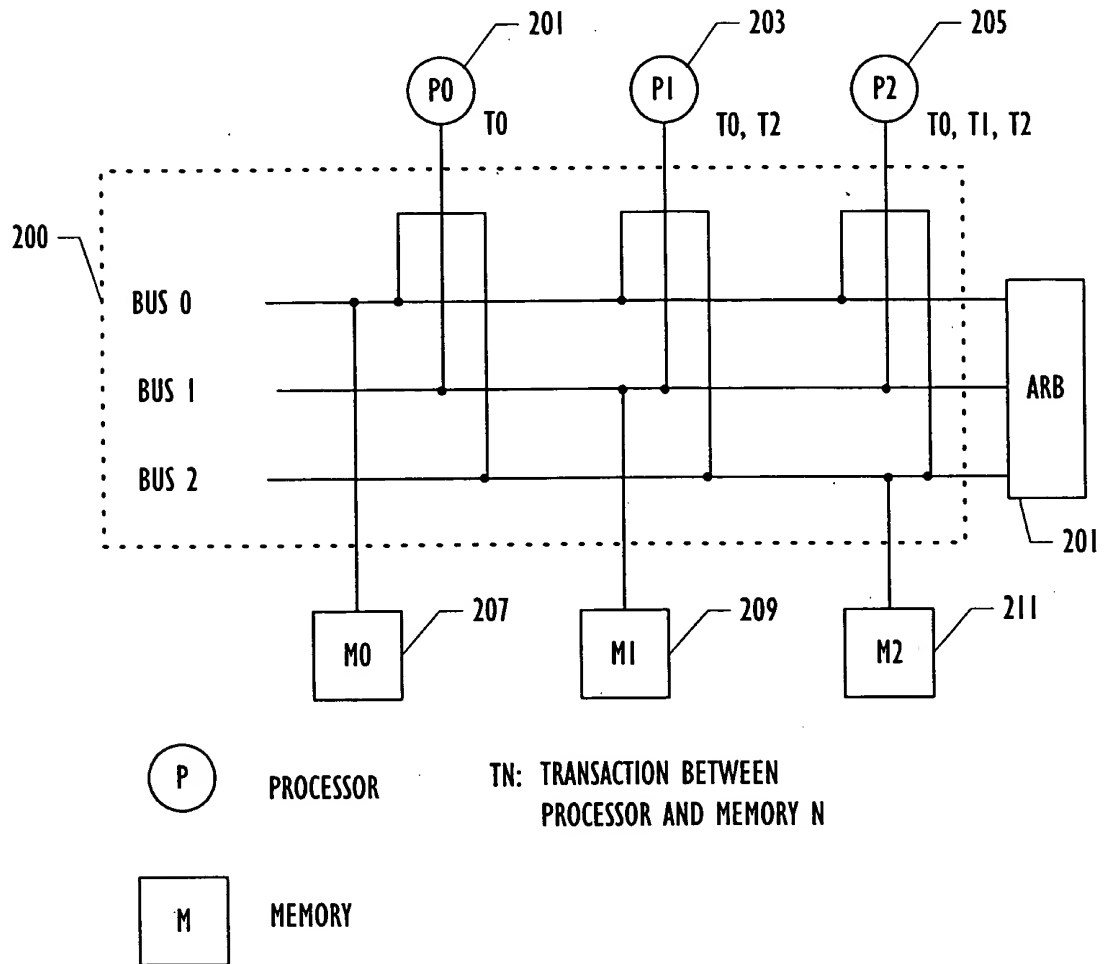


FIG. 2

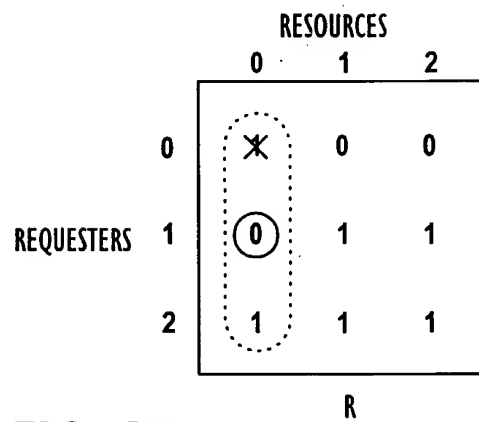


FIG. 3A

I	J	RES	NRQ
1	0	0	nrq[0] = 1 nrq[1] = 2 nrq[2] = 3

○ ROUND-ROBIN POSITION
× GRANTED REQUEST

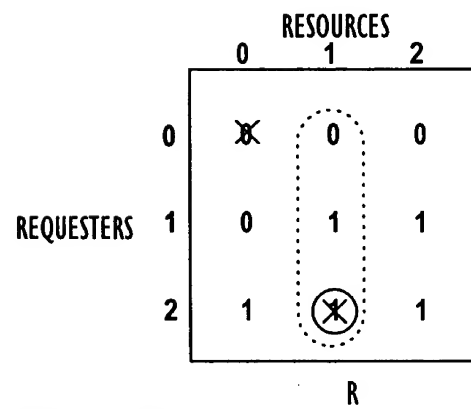


FIG. 3B

I	J	RES	NRQ
1	0	1	nrq[0] = 0 nrq[1] = 2 nrq[2] = 2

○ ROUND-ROBIN POSITION
× GRANTED REQUEST

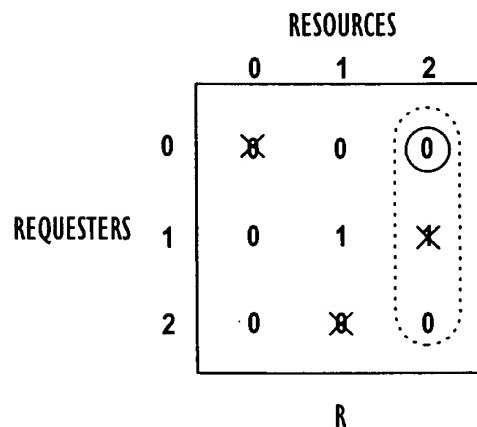


FIG. 3C

I	J	RES	NRQ
1	0	2	nrq[0] = 0 nrq[1] = 1 nrq[2] = 0

○ ROUND-ROBIN POSITION
× GRANTED REQUEST



FIG. 4

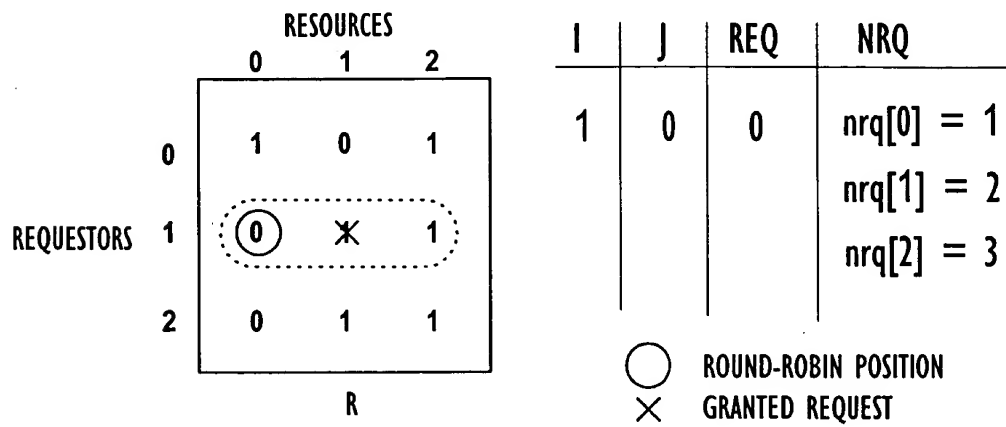


FIG. 5A

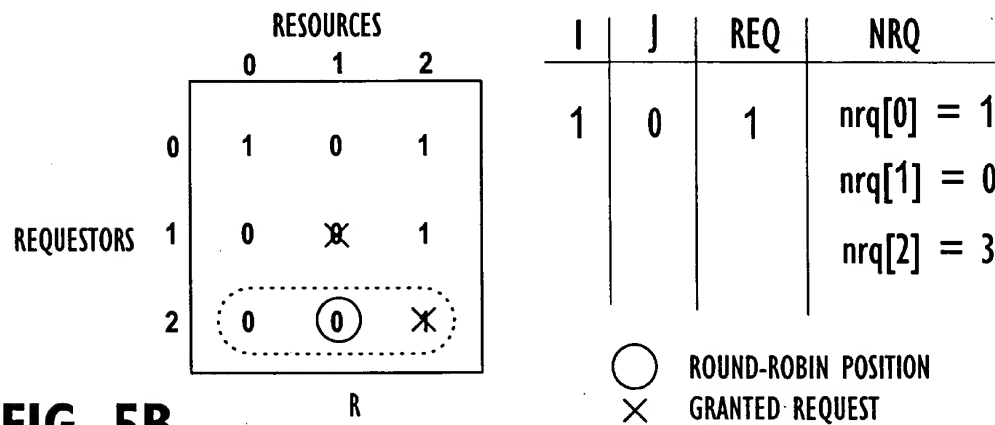


FIG. 5B

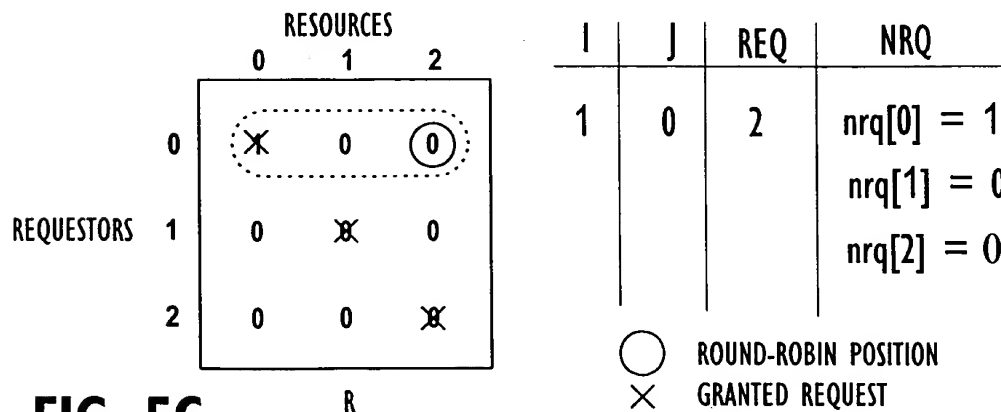


FIG. 5C

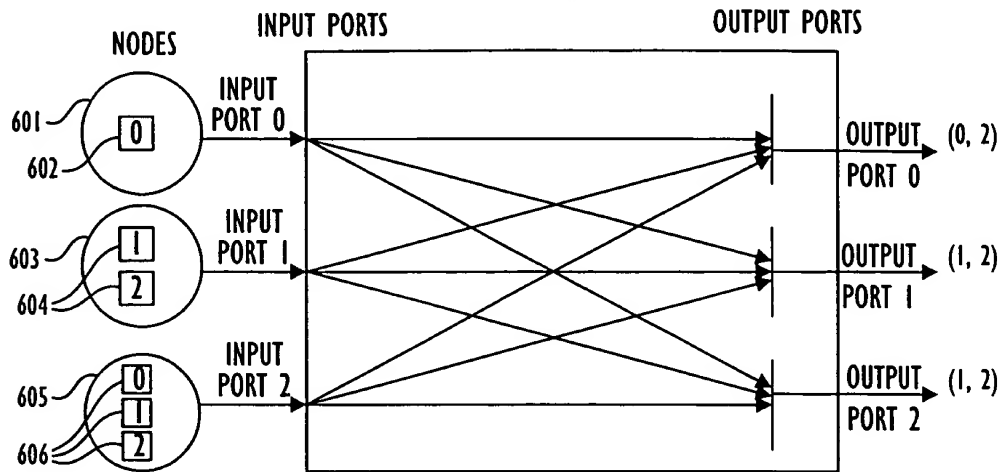


FIG. 6

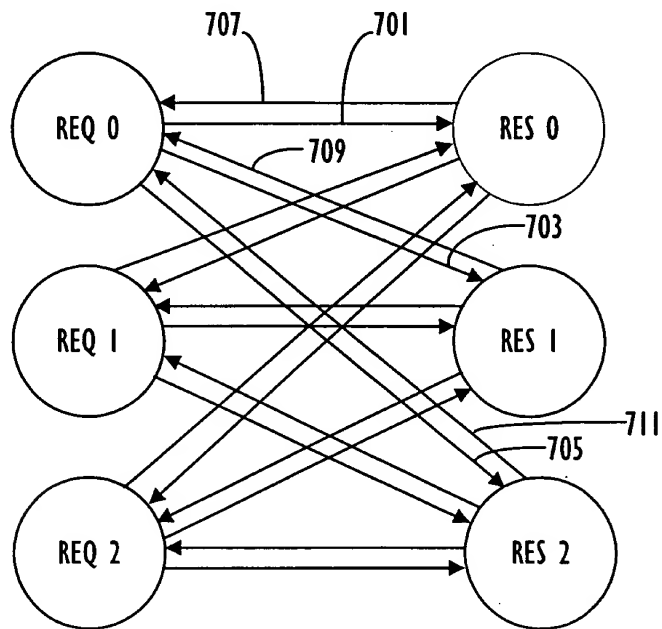


FIG. 7

	Output Port 0	Output Port 1	Output Port 2
REQUEST VECTOR 0	1	0	0
REQUEST VECTOR 1	0	1	1
REQUEST VECTOR 2	1	1	1

FIG. 8